



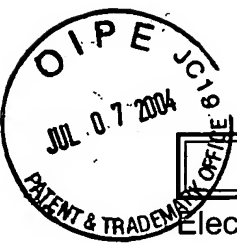
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Electronic Filing System (EFS) Data  
Electronic Patent Application Submission  
USPTO Use Only

EFS ID: 64120  
Application ID: 10656306  
Title of Invention: ASYNCHRONOUS HIDDEN  
REFRESH OF SEMICONDUCTOR  
MEMORY  
First Named Inventor: Wenliang Chen  
Domestic/Foreign Application: Domestic Application  
Filing Date: 2003-09-04  
Effective Receipt Date: 2004-07-07  
Submission Type: Information Disclosure  
Statement  
Filing Type:  
Confirmation number: 8205  
Attorney Docket Number: 5298-19203  
Total Fees Authorized: 180.0  
Payment Category: Deposit Account  
Deposit Account Number: 32769  
Deposit Account Name: Kevin L. Daffer  
Access Code: \*\*\*\*  
RAM Payment Status: RAM success  
RAM User ID: EFSPROD  
RAM Accounting Date: 2004-07-07  
RAM Sequence Number: 34




Digital Certificate Holder: cn=Kevin L. Daffer,ou=Registered Attorneys,ou=Patent and Trademark  
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Certificate Message Digest: 2b488d6b736cf2d7c27f0da13358dae1231c2f0c



TRANSMITTAL

Electronic Version v1.1  
Stylesheet Version v1.1.0

Title of Invention	ASYNCHRONOUS HIDDEN REFRESH OF SEMICONDUCTOR MEMORY															
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<table border="1"><thead><tr><th>Submitted by:</th><th>Elec. Sign.</th><th>Sign. Capacity</th></tr></thead><tbody><tr><td>Kevin L. Daffer Registered Number: 34146</td><td>Kevin L. Daffer</td><td>Attorney</td></tr></tbody></table>			Submitted by:	Elec. Sign.	Sign. Capacity	Kevin L. Daffer Registered Number: 34146	Kevin L. Daffer	Attorney								
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<table><tr><td>Documents being submitted</td><td>Files</td></tr><tr><td>us-ids</td><td>5298-19203_IDS2-usidst.xml</td></tr><tr><td></td><td>us-ids.dtd</td></tr><tr><td></td><td>us-ids.xsl</td></tr><tr><td>us-fee-sheet</td><td>5298-19203_IDS2-usfees.xml</td></tr><tr><td></td><td>us-fee-sheet.xsl</td></tr><tr><td></td><td>us-fee-sheet.dtd</td></tr></table>			Documents being submitted	Files	us-ids	5298-19203_IDS2-usidst.xml		us-ids.dtd		us-ids.xsl	us-fee-sheet	5298-19203_IDS2-usfees.xml		us-fee-sheet.xsl		us-fee-sheet.dtd
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Comments

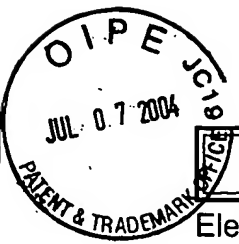


## FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0


Title of Invention	ASYNCHRONOUS HIDDEN REFRESH OF SEMICONDUCTOR MEMORY										
Application Number: 10/656306 											
Date: 2003-09-04											
First Named Applicant: Wenliang Chen											
Attorney Docket Number: 5298-19203											
Art Unit: 2824											
Examiner: Vu A. Le											
<b>TOTAL FEE AUTHORIZED \$180</b>											
Patent fees are subject to annual revisions on or about October 1st of each year.											
<b>BASIC FILING FEE</b>											
<table border="1"><thead><tr><th>Fee Description</th><th>Fee Code</th><th>Amount \$</th><th>Fee Paid \$</th></tr></thead><tbody><tr><td>Submission Of Information Disclosure Stmt Fee</td><td>1806</td><td>180</td><td>180</td></tr></tbody></table>				Fee Description	Fee Code	Amount \$	Fee Paid \$	Submission Of Information Disclosure Stmt Fee	1806	180	180
Fee Description	Fee Code	Amount \$	Fee Paid \$								
Submission Of Information Disclosure Stmt Fee	1806	180	180								
<b>AUTHORIZED BILLING INFORMATION</b>											
The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:											
Deposit account number: 032769											
Access Code ****											
Deposit name: Conley Rose, P.C.											
Deposit authorized name: Kevin L. Daffer											
Signature: Kevin L. Daffer											
Date (YYYYMMDD): 2004-07-07											



## ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

<b>Title of Invention</b>	<b>ASYNCHRONOUS HIDDEN REFRESH OF SEMICONDUCTOR MEMORY</b>																												
<p>Application Number: 10/656306 </p> <p>Confirmation Number: 8205</p> <p>First Named Applicant: Wenliang Chen</p> <p>Attorney Docket Number: 5298-19203</p> <p>Art Unit: 2824</p> <p>Examiner: Vu A. Le</p> <p>Search string: ( 6392958 or 6590828 ).pn.</p> <p><b>US Patent Documents</b></p> <p>Note: Applicant is not required to submit a paper copy of cited US Patent Documents</p> <table border="1"><thead><tr><th>init</th><th>Cite.No.</th><th>Patent No.</th><th>Date</th><th>Patentee</th><th>Kind</th><th>Class</th><th>Subclass</th></tr></thead><tbody><tr><td></td><td>1</td><td>6392958</td><td>2002-05-21</td><td>Lee</td><td></td><td></td><td></td></tr><tr><td></td><td>2</td><td>6590828</td><td>2003-07-08</td><td>Kim</td><td></td><td></td><td></td></tr></tbody></table> <p><b>Signature</b></p> <table border="1"><thead><tr><th>Examiner Name</th><th>Date</th></tr></thead><tbody><tr><td></td><td></td></tr></tbody></table>		init	Cite.No.	Patent No.	Date	Patentee	Kind	Class	Subclass		1	6392958	2002-05-21	Lee					2	6590828	2003-07-08	Kim				Examiner Name	Date		
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